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In the Specification:

Please delete paragraph 32 and replace it with the following paragraph:

[32] Figure Figures 55A-5E areis substantially a schematic diagrams diagram of an electrical

circuit for driving the light emitting diodes.

Please delete paragraph 56 and replace it with the following paragraph:

[56] Turning now to figures 5A-5E, schematic diagramsfigure 5, a schematic diagram of at

least a portion of one embodiment of controller 32 and board 63 are is shown. Controller 32 may

be adapted to control the operation of the light emitting diodes 88. Controller 32 may comprise

integrated circuits U1, U2, and U3, and a power supply 120. Integrated circuits U2 and U3 are

8-bit shift registers. U2 and U3 contain an 8-bit serial-in, parallel-out shift register that feeds an

8-bit D-type storage register. Integrated circuit U1 is a 4-bit microcontroller. Integrated circuits

U2 and U3 are connected to the matrix 126 of LEDs 88. Resistors R4-R19 are connected

between each row of the LEDs and 24 volts DC. Microcontroller U1 may be programmed to

turn on rows of LEDs 132 in a pre-determined sequence for a predetermined period of time.

Controller 32 may be chained together to control more matrices 126 of LEDs using connectors

P2 and P3. A connector P1 provides a connection from the microcontroller U1 to power,

ground, and an external trigger. Power supply 120 provides power to integrated circuits U1, U2,

and U3.

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CONCLUSION

The Applicants submit that the present application is in condition for allowance. If the Examiner has any questions regarding the application or this response, the Examiner is encouraged to call the Applicants' attorney, Ryan A. Heck, at (775) 826-6160.

10 March 8, 2004

Ryan A. Heck, Ph.D. Attorney for Applicants Registration Number: 51,795

Respectfully submitted,

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